

FIG. I

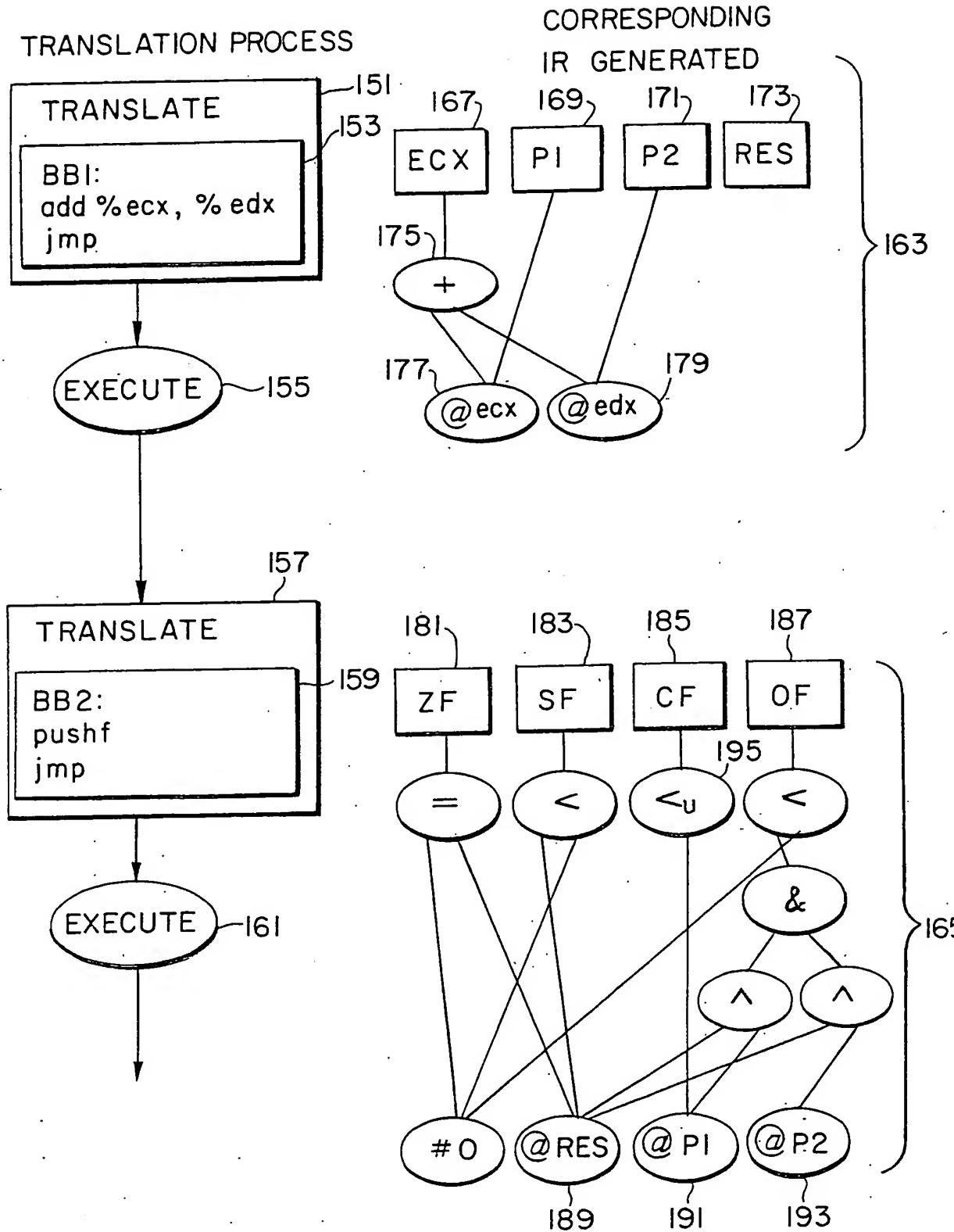


FIG. 2

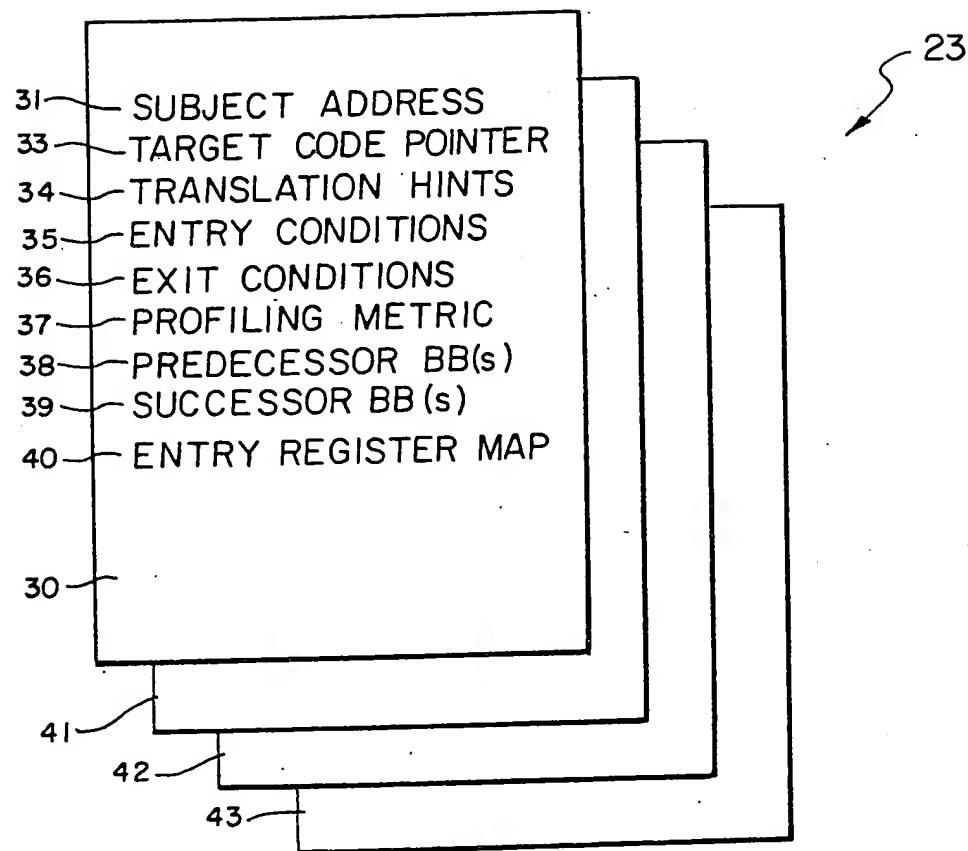


FIG. 3

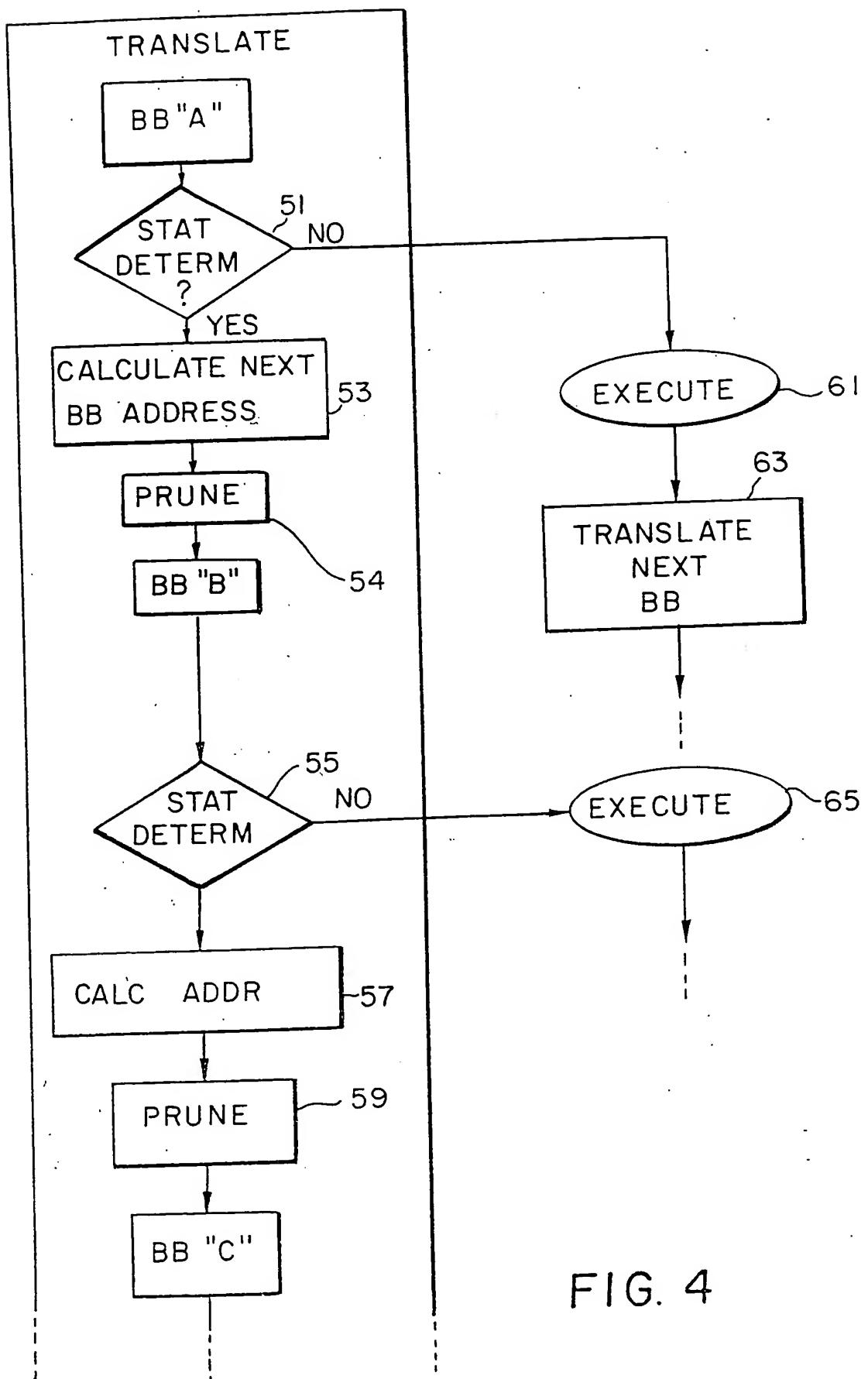


FIG. 4

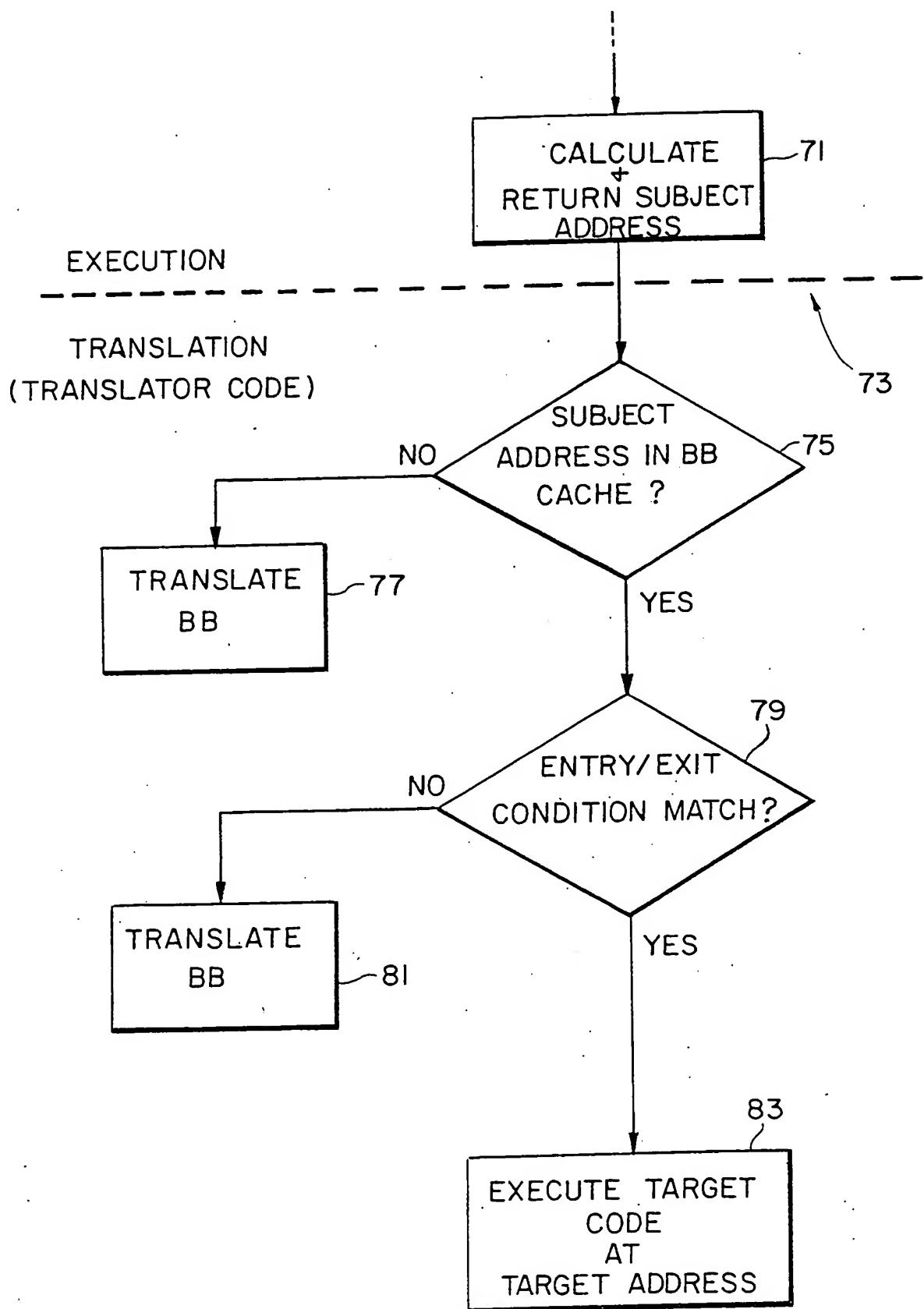


FIG. 5

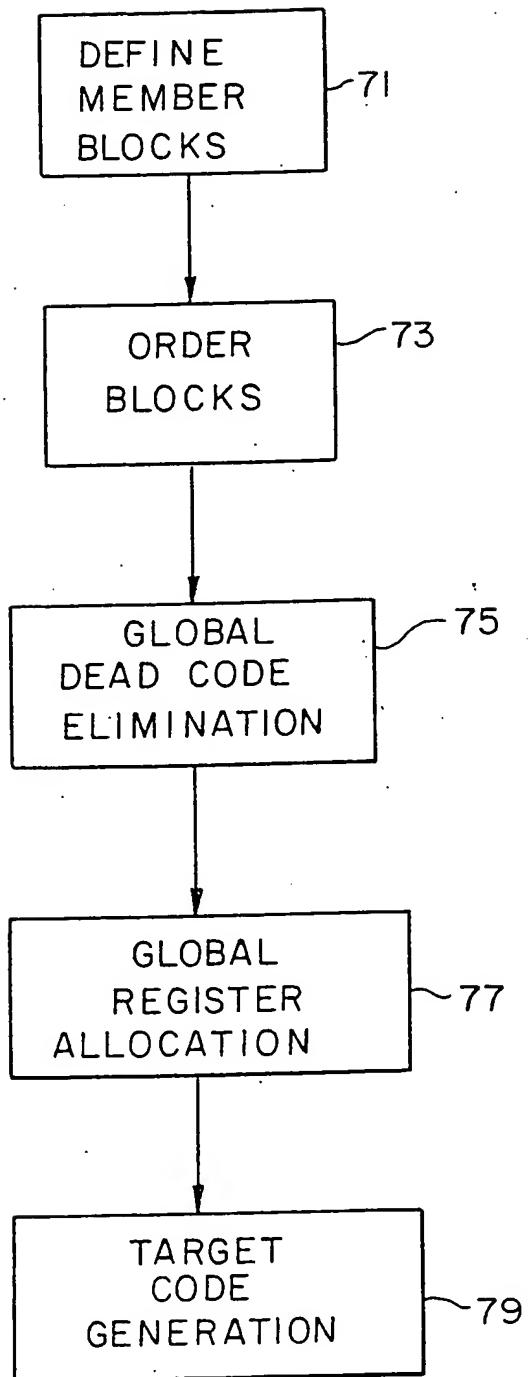


FIG. 6

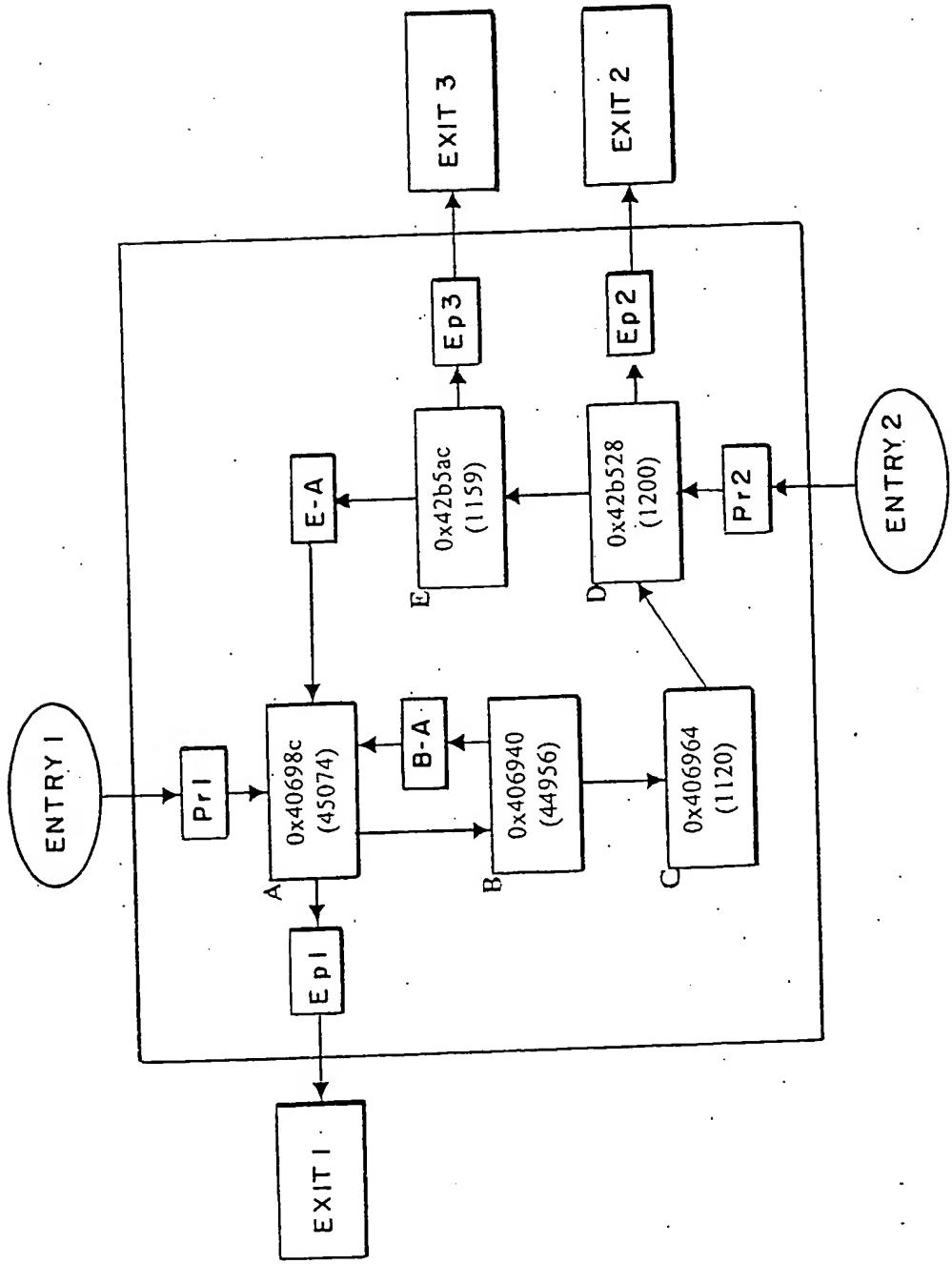


FIG. 7

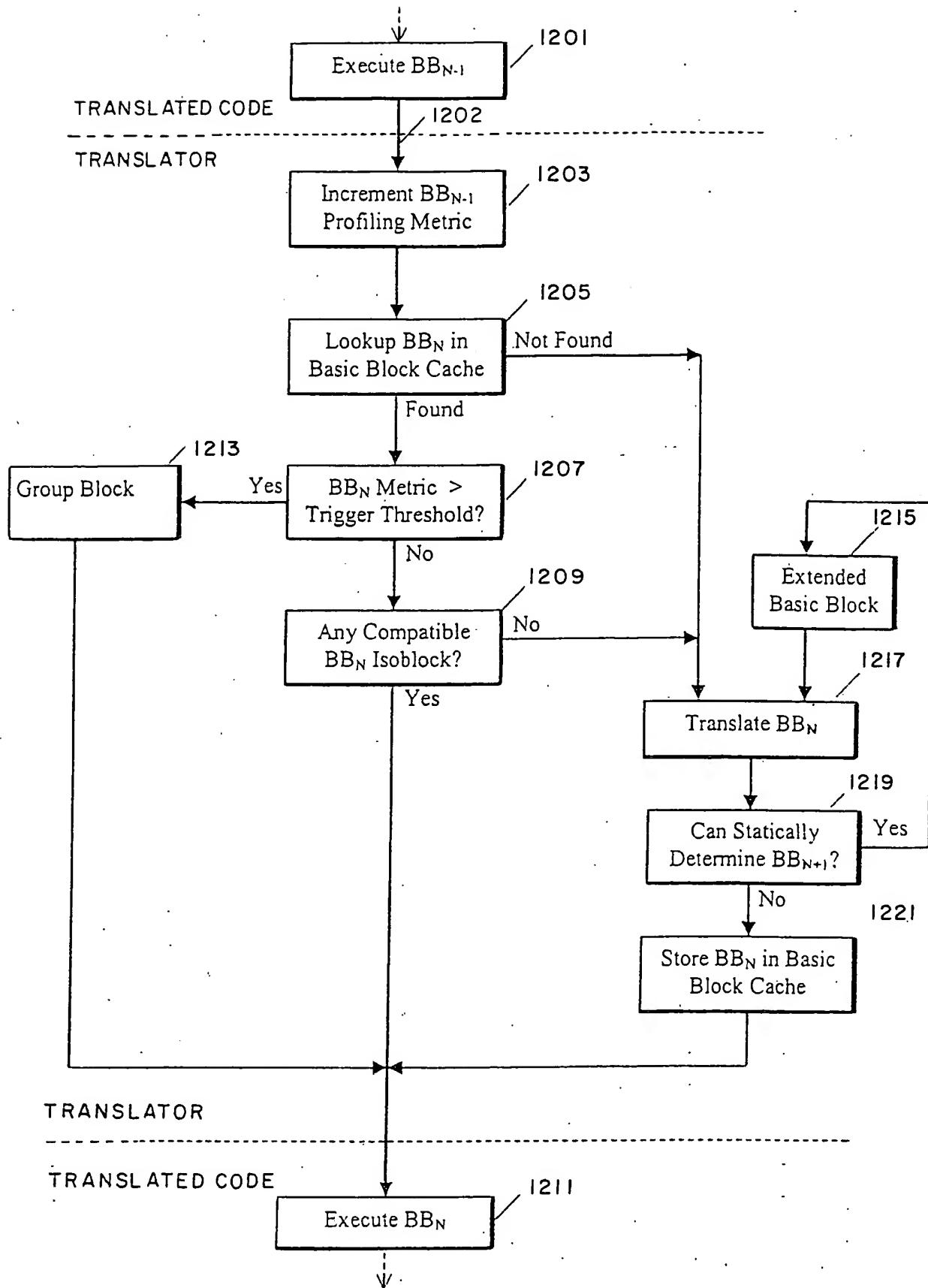
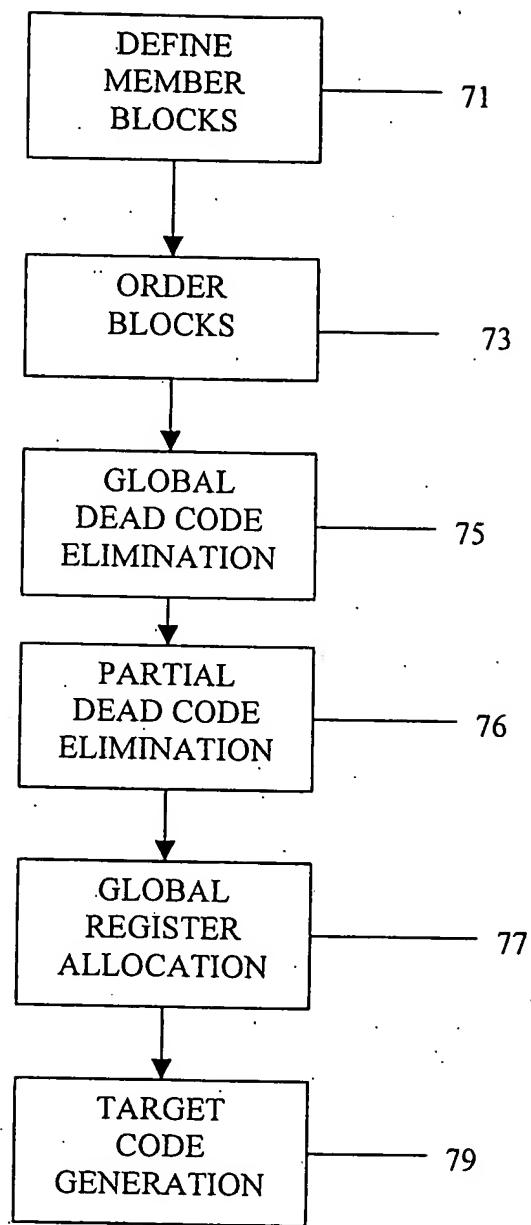
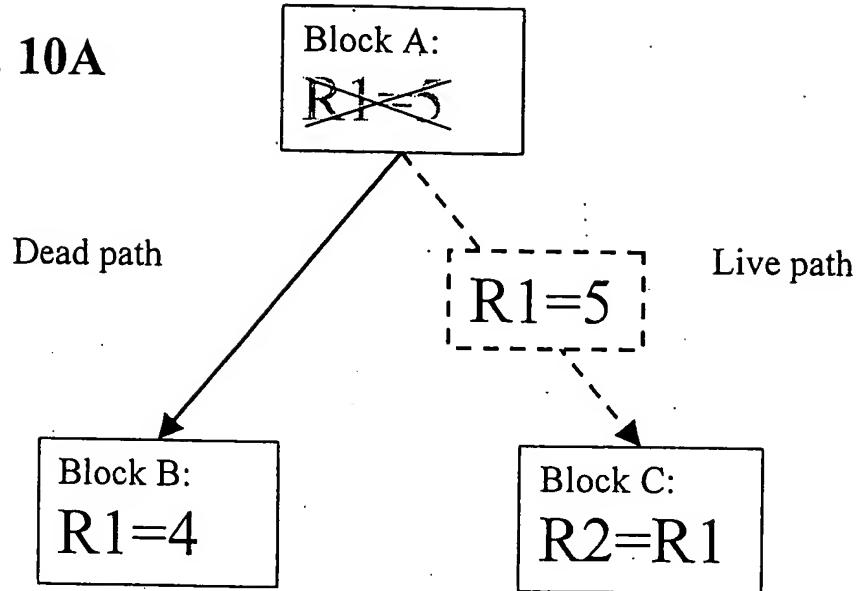


FIG. 8

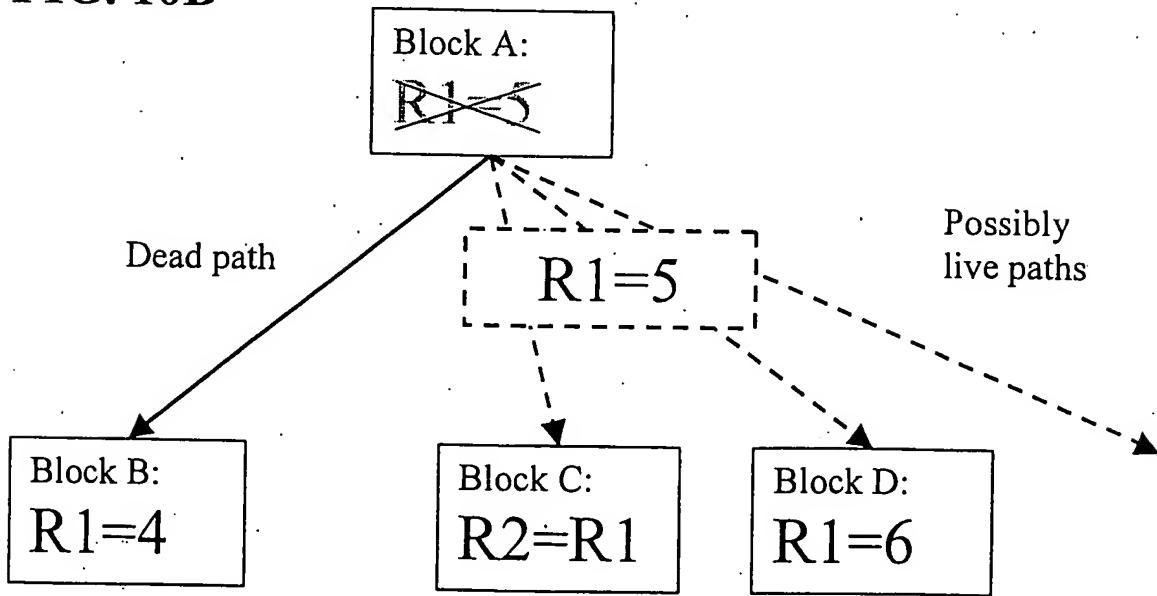


**FIG. 9**

**FIG. 10A**



**FIG. 10B**



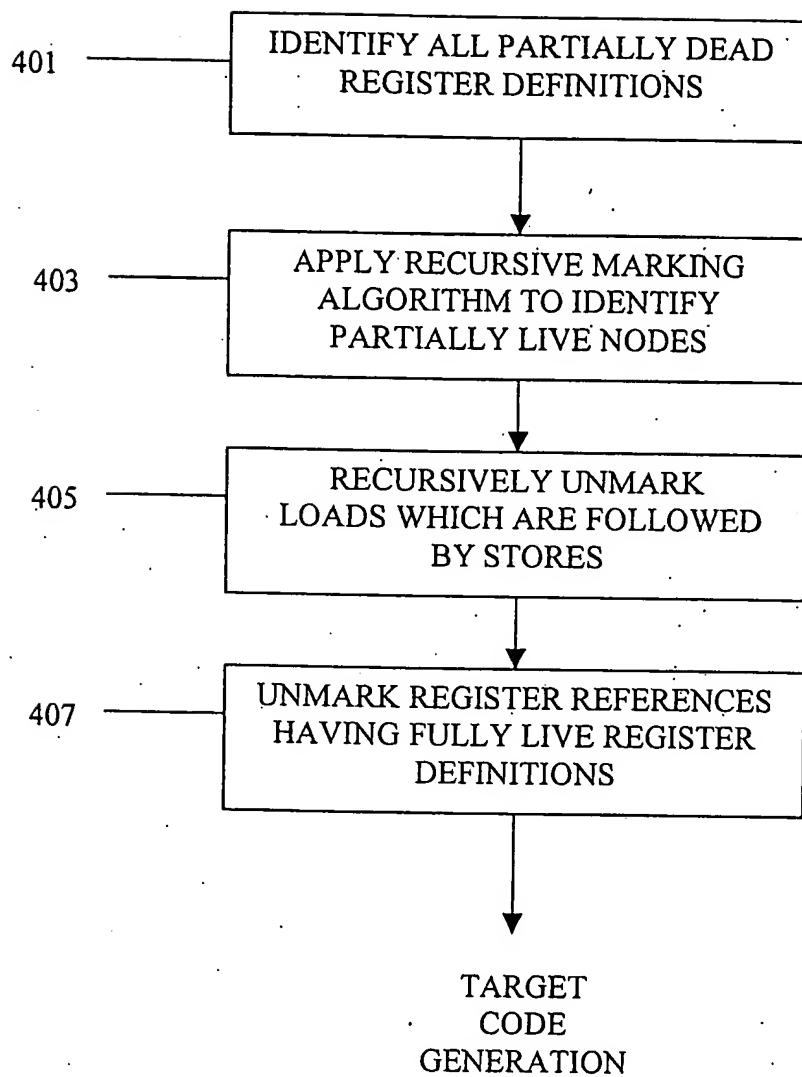
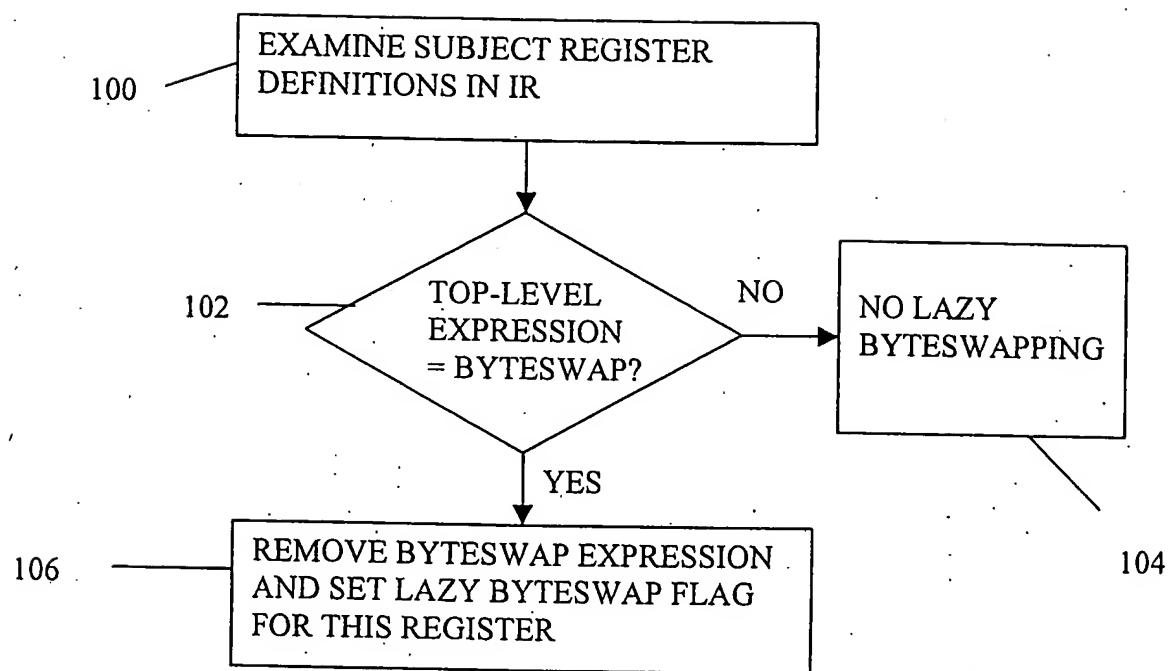


FIG. 11

FIG. 12



# FIG. 13A

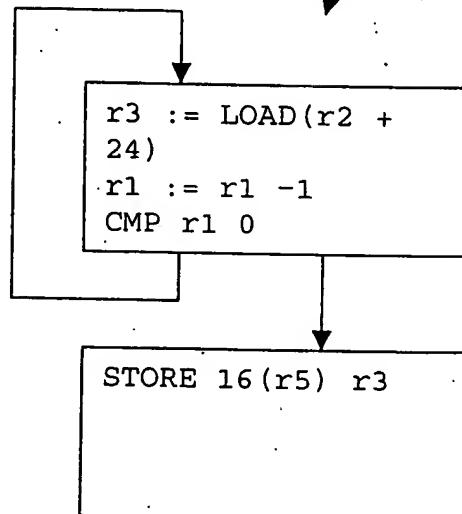
Subject Code

200

```
LABEL1    r3 := LOAD(r2 + 24)
          r1 := r1 -1
          CMP r1 0
          BC LABEL1
          STORE 16(r5) r3
```

Group Block

202

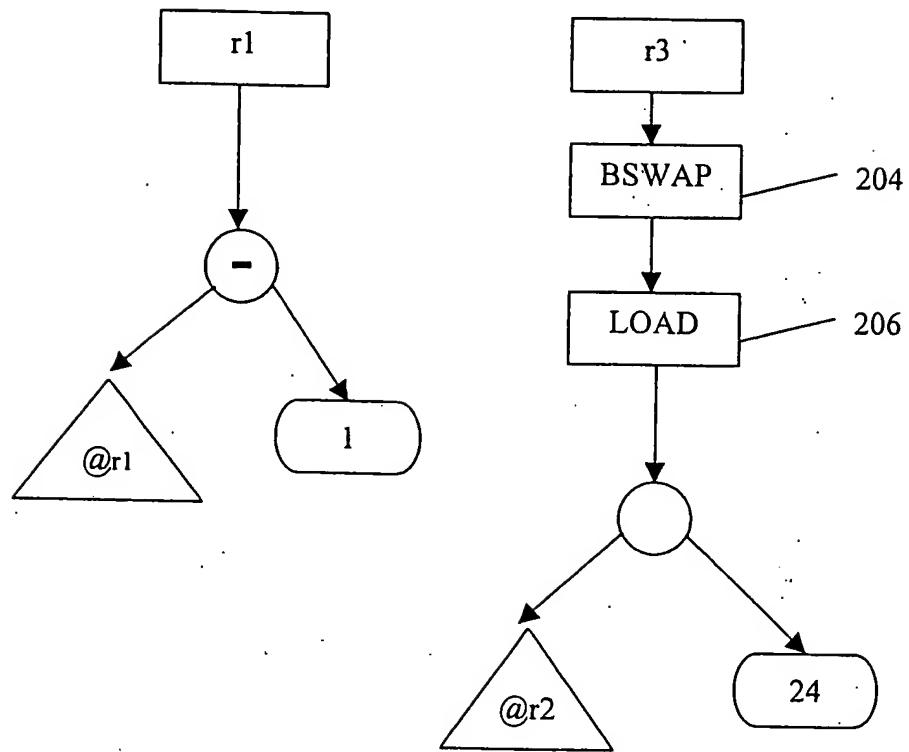


Block 1

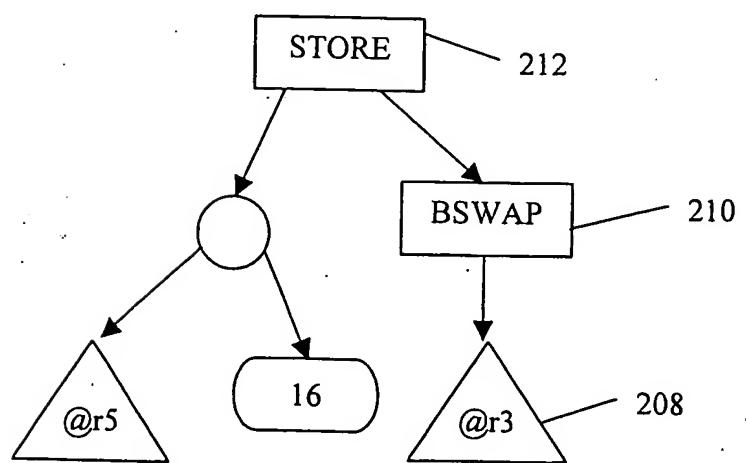
Block 2

FIG. 13B

## Block 1 IR Without Lazy Byteswapping

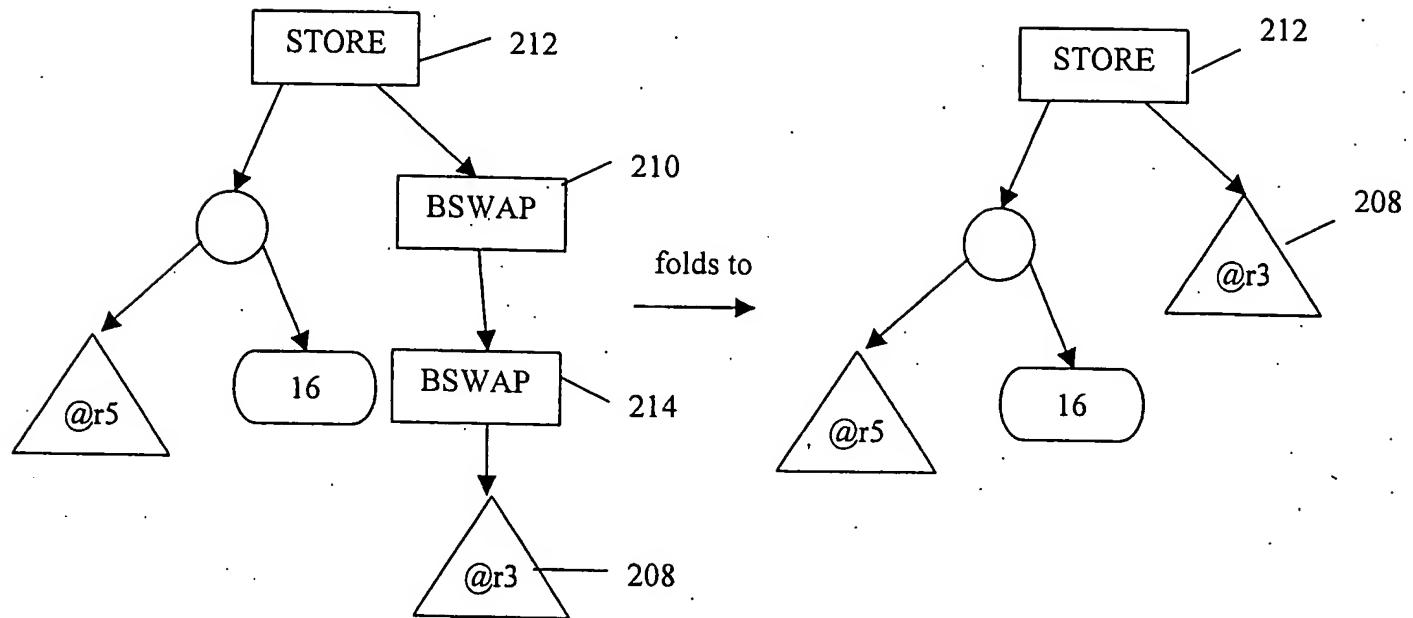


## Block 2 IR Without Lazy Byteswapping



# FIG. 13C

Block 2 IR With Lazy Byteswapping



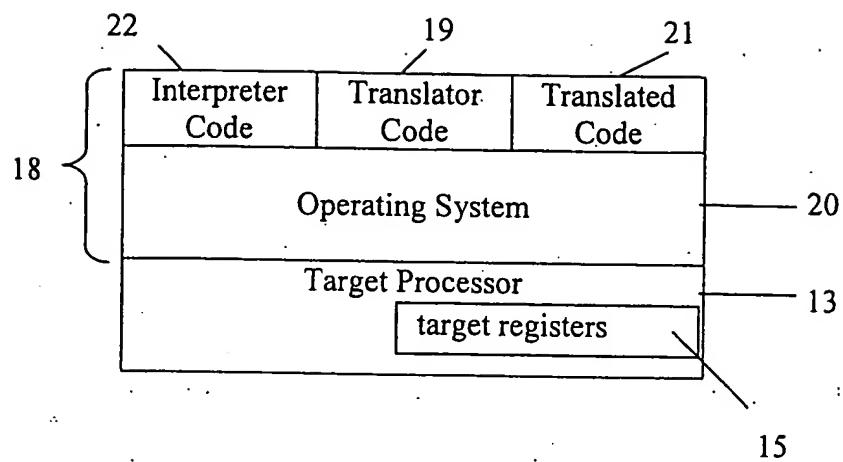


FIG. 14

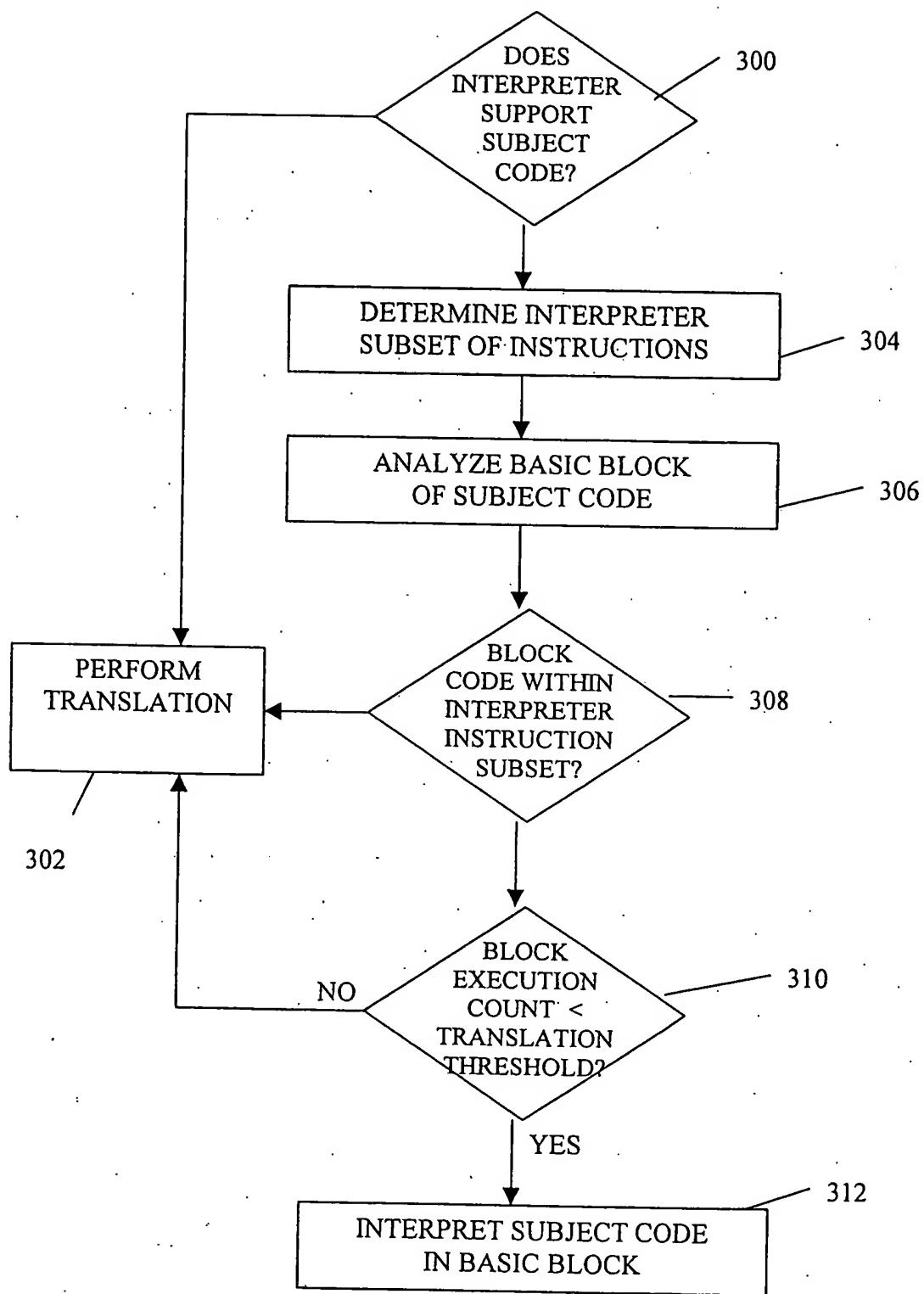


FIG. 15